

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		display\$6 and ((digital adj analog or D/A) same convert\$6 and CMOS same latch same circuit\$6 and reference\$5 same voltage\$1 and line\$1) and comparat\$6 and n adj bit and digital and data and signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:30
		display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6 and (n adj bit and digital and data and signal\$6) and ("2" adj n and step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:11
		display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6) and n adj bit and digital and data and signal\$6) and ("2" adj n and step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 21:48
		(digital adj analog or D/A) same convert\$6 same circuit\$6 and (plural\$6 or multipel\$6) same step\$6 same select\$6 same connect\$6 and (plural\$6 or multiple\$6) same reference\$5 same voltage\$1 same line\$1 and includ\$6 and n same serial\$6 same connect\$6 same analog switch\$6 same polariz\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 16:50
		(digital adj analog or D/A) same convert\$6 same circuit\$6 and (plural\$6 or multipel\$6) same step\$6 same select\$6 same connect\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 16:48
		digital-analog same convert\$6 same circuit\$6 and (plural\$6 or multipel\$6) same step\$6 same select\$6 same connect\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 16:48

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		"1." (CURRENTLY AMENDED) A digital-analog converter circuit for converting an n-bit (n is an integer of "2" or more) digital data signal comprising 2n step select units connected across 2n reference voltage lines, each step select unit including n serially connected analog switches polarized to match the alogic state of each data signal n bit (n is an integer of "2" or more), and 2n tone select units respectively connected across the outputs of each of the 2n reference voltage lines bit of the n-bit digital data "signal54." (CURRENTLY AMENDED) A latch circuit with including a CMOS latch cell having two input sections as a basic structure, wherein said latch circuit has comprising a first switch and a second switch to respectively select a first and second power supply having different voltages and installed on at least one of the a positive power side or the a negative power side of said CMOS latch cell and, having a control means to control switching of said first and second switches according to the periods of the a latch operation period and an output operation period of said CMOS latch "cell." first and second switches are "transistors." a plurality of said latch circuits are installed and, said first switch and said second switch are jointly shared by said plurality of sampling latch "circuits." latch circuit is fabricated by utilizing thin film transistors formed on a glass "substrate." latch circuit is fabricated by utilizing thin film transistors formed on a silicon substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 16:40
S1	395	(digital adj analog or D/A) same convert\$6 same circuit\$6 and (plural\$6 or multipel\$6) same step\$6 same select\$6 same connect\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 16:49
S2	32	(digital adj analog or D/A) same convert\$6 same circuit\$6 and (plural\$6 or multipel\$6) same step\$6 same select\$6 same connect\$6 and (plural\$6 or multiple\$6) same reference\$5 same voltage\$1 same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 16:56

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S3	1	(digital adj analog or D/A) same convert\$6 same circuit\$6 and (plural\$6 or multipel\$6) same step\$6 same select\$6 same connect\$6 and (plural\$6 or multiple\$6) same reference\$5 same voltage\$1 same line\$1 and includ\$6 and n same serial\$6 same connect\$6 same analog same switch\$6 same polariz\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 16:52
S4	32	(digital adj analog or D/A) same convert\$6 same circuit\$6 and (plural\$6 or multipel\$6) same step\$6 same select\$6 same connect\$6 and (plural\$6 or multiple\$6) same reference\$5 same voltage\$1 same line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 21:43
S5	6	("5066883"   "5532544"   "5610478"   "5622634"   "5659329"   "5886988"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/03 21:24
S6	14	("6664943").URPN.	USPAT	OR	ON	2007/06/03 21:25
S7	804	display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6) and (("2" adj n and step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 21:47
S8	220	display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6) and (n adj bit and digital and data and signal\$6) and (("2" adj n and step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 21:48
S9	220	display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6 and n adj bit and digital and data and signal\$6) and (("2" adj n and step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 21:49
S10	0	display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6 and n adj bit and digital and data and signal\$6) and (("2" adj n adj step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 21:50

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S11	6	display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6 and n adj bit and digital and data and signal\$6) and ("2" adj n same step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:38
S12	0	display and digital-analog and convert\$6 and circuit\$6 and convert\$6 and n-bit and digital and data and signal\$6 and 2n and step and select\$6 and connect\$6 and across and 2n and reference\$6 and voltage\$6 and line\$1 and each and step\$6 and select\$6 and unit and includ\$6 and n and serial\$6 and connect\$6 and analog and switch\$6 and polariz\$6 and match\$6 and logic and state and each and data and signal and n-bit and 2n and tone\$6 and select\$6 and unit\$1 and respective\$6 and connect\$6 and across and output\$6 and each and 2n and reference\$6 and voltage\$1 and line\$1 and bit\$1 and n-bit and digital and data and signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:19
S13	207167	display\$6 and (digital same analog or D/A)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:19
S14	132733	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:20
S15	855	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:23
S16	8	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 2n and step and select\$6 and connect\$6 and across and 2n and reference\$6 and voltage\$6 and line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:21

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S17	0	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 2n and step and select\$6 and connect\$6 and across and 2n and reference\$6 and voltage\$6 and line\$1 and each and step\$6 and select\$6 and unit and includ\$6 and n and serial\$6 and connect\$6 and analog and switch\$6 and polariz\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:22
S18	0	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 2n and step and select\$6 and connect\$6 and across and 2n and reference\$6 and voltage\$6 and line\$1 and each and step\$6 and select\$6 and unit and includ\$6 and n and serial\$6 and connect\$6 and analog and switch\$6 and polari\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:23
S19	1	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 2n and step and select\$6 and connect\$6 and across and 2n and reference\$6 and voltage\$6 and line\$1 and step\$6 and select\$6 and unit and includ\$6 and n and serial\$6 and connect\$6 and analog and switch\$5 and polari\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:23
S20	53	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 345/98.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:24
S21	50	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 345/98.ccls. and switch\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:24
S22	39	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 345/98.ccls. and switch\$6 and reference\$6 same voltage\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:25

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S23	1	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 345/98.ccls. and switch\$6 and reference\$6 same voltage\$1 and 2n same step\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:25
S24	3	display\$6 and (digital same analog or D/A) and convert\$6 and circuit\$6 and convert\$6 same n-bit and digital and data and signal\$6 and 345/98.ccls. and switch\$6 and reference\$6 same voltage\$1 and (2n and step\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:26
S25	2	"6987474".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 09:10
S26	1	"6987474".pn. and comparat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/03 22:31
S27	6	display\$6 and ((digital adj analog or D/A) and convert\$6 and circuit\$6 and n adj bit and digital and data and signal\$6) and ("2" adj n same step\$6) and select\$6 and connect\$6) and ("2" adj n and reference\$5 and voltage\$1 and line\$1) and comparat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:28
S28	1	"6911926".pn. and reference same voltage\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 09:09
S29	0	"6987474".pn. and (2n and step\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 09:10
S30	0	"6987474".pn. and (2n and step\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 09:11
S31	1	"6911926".pn. and (2n and step\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 09:15

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S32	1	"6911926".pn. and (2n and step\$6) and reference same voltage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 09:24
S33	1	"6911926".pn. and logic same state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 09:18
S34	1	"6911926".pn. and reference same voltage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 11:14
S35	1	"6911926".pn. and threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 11:14
S36	1	"6911926".pn. and (threshold or sub same threshold)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 11:48
S37	0	"6911926".pn. and cmos same latch\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 11:48
S38	1	"6911926".pn. and (cmos and latch\$6 same circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:21
S39	2	"6987474".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:22
S40	0	"6987474".pn. and cmos same latch\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:22
S41	0	"6987474".pn. and cmos and latch\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:22

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S42	1	"6987474".pn. and cmos and latch\$6 and circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:23
S43	122	display\$6 and ((digital adj analog or D/A) and convert\$6 and CMOS same latch same circuit\$6 and reference\$5 and voltage\$1 and line\$1) and comparat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:29
S44	110	display\$6 and ((digital adj analog or D/A) same convert\$6 and CMOS same latch same circuit\$6 and reference\$5 and voltage\$1 and line\$1) and comparat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:29
S45	77	display\$6 and ((digital adj analog or D/A) same convert\$6 and CMOS same latch same circuit\$6 and reference\$5 same voltage\$1 and line\$1) and comparat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:30
S46	14	display\$6 and ((digital adj analog or D/A) same convert\$6 and CMOS same latch same circuit\$6 and reference\$5 same voltage\$1 and line\$1) and comparat\$6 and (n adj bit and digital and data and signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:31
S47	1	display\$6 and ((digital adj analog or D/A) same convert\$6 and CMOS same latch same circuit\$6 and reference\$5 same voltage\$1 and line\$1) and comparat\$6 and (n adj bit and digital and data and signal\$6) and (("2" adj n same step\$6) and select\$6 and connect\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:31
S48	2	"5371517".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:47
S49	0	"5371517".pn. and cmos same latch same citrcuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:48
S50	0	"5371517".pn. and cmos same latch\$6 same citrcuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:49



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S51	1	"5371517".pn. and CMOS same latch same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 12:49
S52	2	"5572221".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 13:19
S53	0	"5572221".pn. and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 13:20
S54	0	"5572221".pn. and substrate\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 13:20
S55	2	"6274869".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 16:18
S56	1	"6274869".pn. and start\$6 and shift same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 17:16
S57	1	"6274869".pn. and resistor and transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 17:19
S58	1	"6274869".pn. and resistor same switch\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/04 17:19